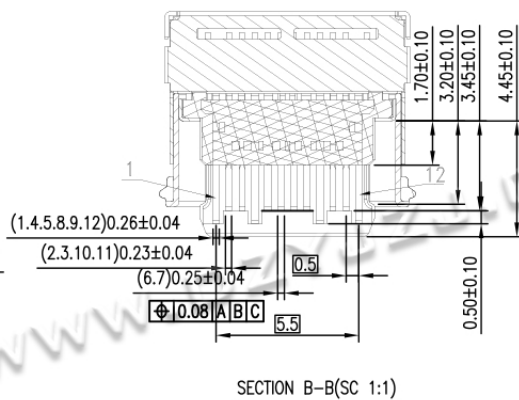
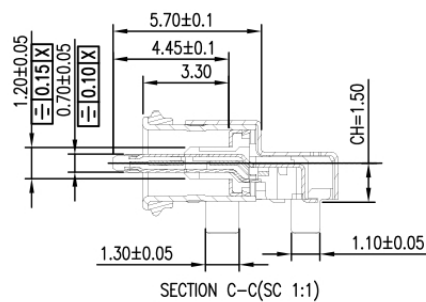
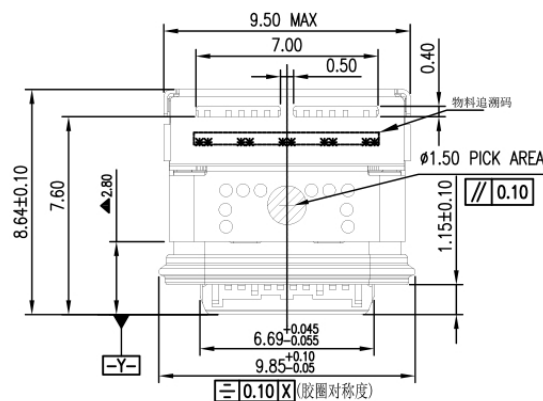
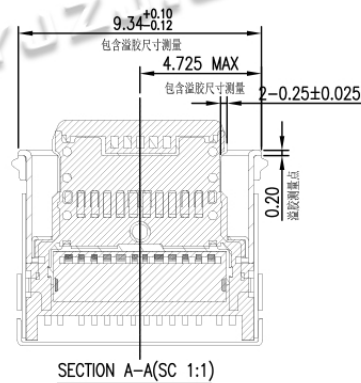
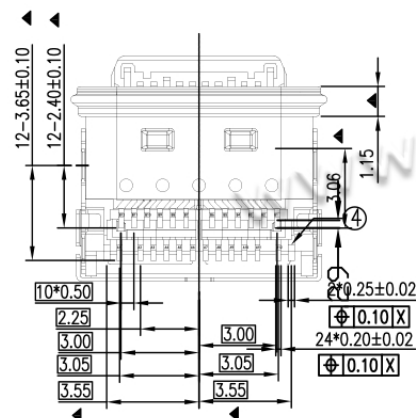
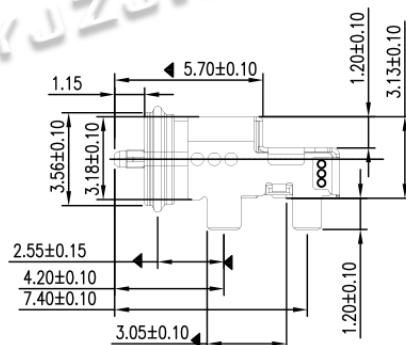
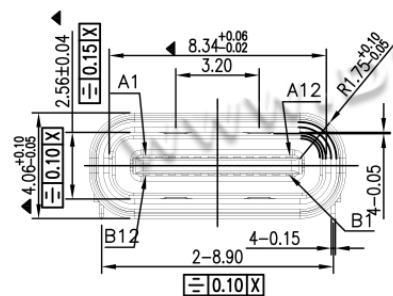




1. ELECTRICAL CHARACTERISTICS:
 - 1.1 CONTACT RESISTANCE: $40\text{m}\Omega$ Max FOR INITIAL, $10\text{m}\Omega$ CHANGE AFTER TEST.
MEASURE AT 20mA , 100mV .
 - 1.2 CONTACT CURRENT RATING: 7A FOR COLLECTIVELY POWER SUPPLY
PIN(PIN1, A4, A9, A12, B1, B4, B9, B12): 1.25A FOR VCONN PIN; 0.25A FOR OTHER SIGNAL PIN.
 - 1.3 DIELECTRIC WITHSTANDING VOLTAGE: 100V AC R.M.S.
 - 1.4 INSULATION RESISTANCE $100\text{M}\Omega$ Min
 - 1.5 OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
2. MECHANICAL CHARACTERISTICS:
 - 2.1 MATING FORCE: $5\sim 20\text{N}$
 - 2.2 UNMATED FORCE: $8\sim 20\text{N}$ INITIAL TEST; $6\sim 20\text{N}$ DURABILITY AFTER TEST
 - 2.3 DURABILITY: $10,000$ CYCLES

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS



PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTxp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTxn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VRES	Bus Power	B9	VRES	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use(SBU)	B5	CC2	Configuration Channel
A9	VRES	Bus Power	B4	VRES	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return



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TOLERANCE:

X.X	±0.30
X.XX	±0.25
X.XXX	±0.15

UNIT: mm [inch]

SCALE:1:1	SIZE: A4
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DRAWN BY :

CHECKED BY:

APPROVED BY _____

邱敏

DATE :

DATE :	
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DATE :

2014-02-23

PART NAME:	
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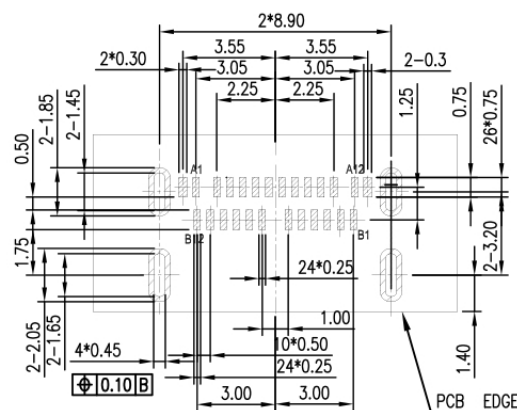
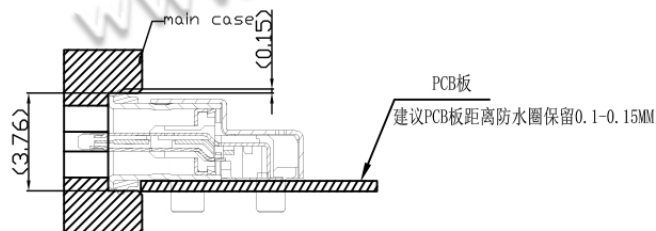
USB TYPE C 防水IP68母座24P 板上双贴SMT脚长1.2

PART NO.	HYC409-USBC24-120
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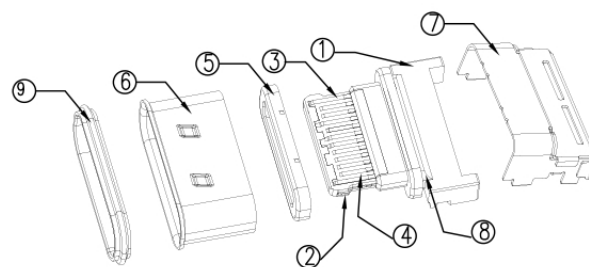
MOLD NO.	
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DRAW NO:	HYC-2212191031
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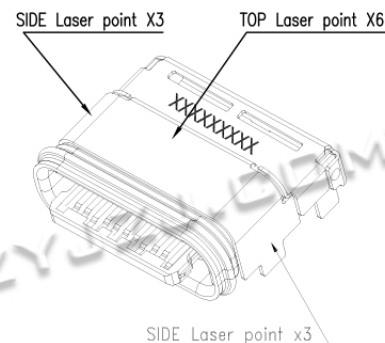
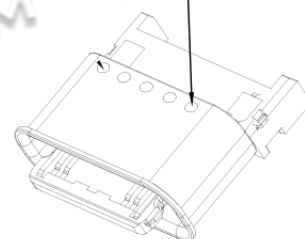
SHEET NO.	1 OF 1
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RECOMMENDED PCB LAYOUT
(Tolerance=±0.05)SECTION A-A
PANEL CUT-OUT REFERENCE

9	密封圈	1PCS	硅胶, 依照客户需求	
8	胶水	1PCS	环氧胶 黑色	
7	外壳	1PCS	SUS304, T=0.15, 镀镍50u" MIN.	
6	内拉伸壳	1PCS	SUS304, T=0.20.	
5	EMI拉伸壳	1PCS	SUS304, T=0.12, 镀镍50u" MIN.	
4	上排端子	1SET	C18400, T=0.12, 底层镀镍 50-100u", 功能接触区单面	
3	下排端子	1SET	刷镀 Au 15u", 焊接区双面镀金 G/F.	
2	中板	1PCS	SUS301-1/4 H, 镀镍50u" MIN.	
1	塑胶	1PCS	PA46 BLACK	
项目	组成部件	用量	材质/处理	备注



Front&Back Laser point 2X5



说明:

1. Colplanarity: 0.08最大红外线前., 0.10mm MAX
2. 舌PIN的位置: IR前: 0.08mm MAX; IR后0.12mm MAX;
3. P/N:
4. 包装方式: 载带包装
5. 接触区和焊接区无毛刺, 除特说明, 冲压毛刺为0.05mm MAX.
6. 未标注尺寸公差为依自由公差为准.
7. 成品端子平面度必须100%检测.
8. 成品可靠性测试要求参照规格书.
9. 产品满足 ROHS 2.0

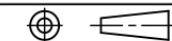


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TOLERANCE:

X.X ±0.30
X.XX ±0.25
X.XXX ±0.15

X.* ±2" X.X* ±0.5"



UNIT: mm [inch]

SCALE: 1:1 SIZE: A4

DRAWN BY:

陈一鸣

DATE:

2014-02-23

CHECKED BY:

马跃

DATE:

2014-02-23

APPROVED BY:

邱敏

DATE:

2014-02-23

PART NAME:

USB TYPE C 防水IP68插座24P 板上双贴SMT脚长1.2

PART NO.

HYC409-USBC24-120

MOLD NO.

DRAW NO: HYC-2212191031

SHEET NO.

1 OF 1